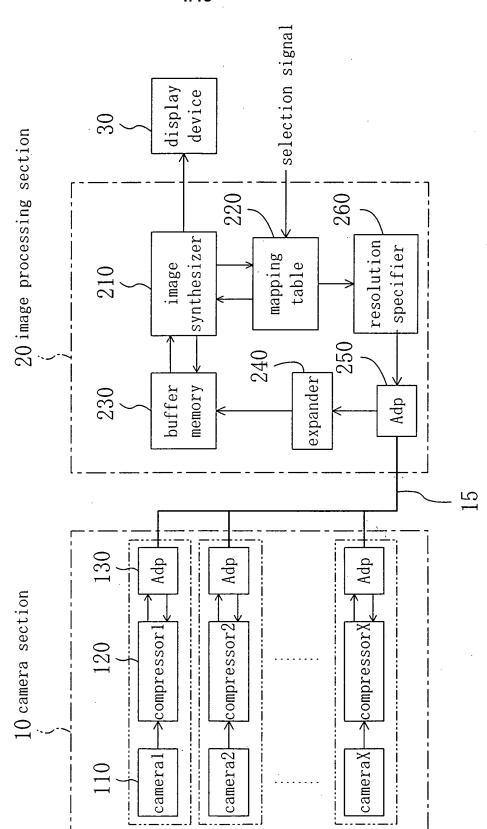
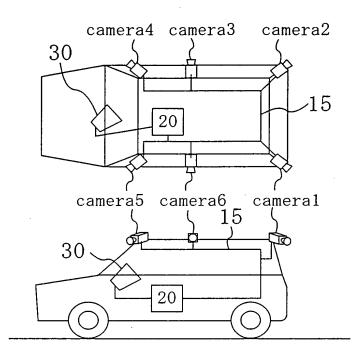
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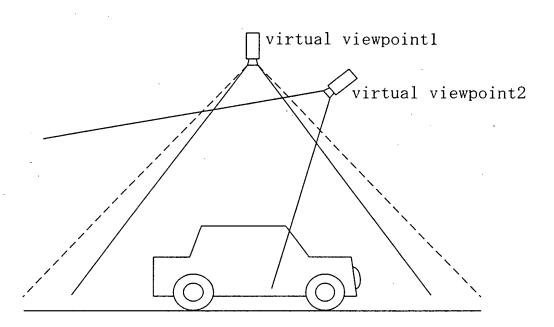


Title: MONITORING SYSTEM

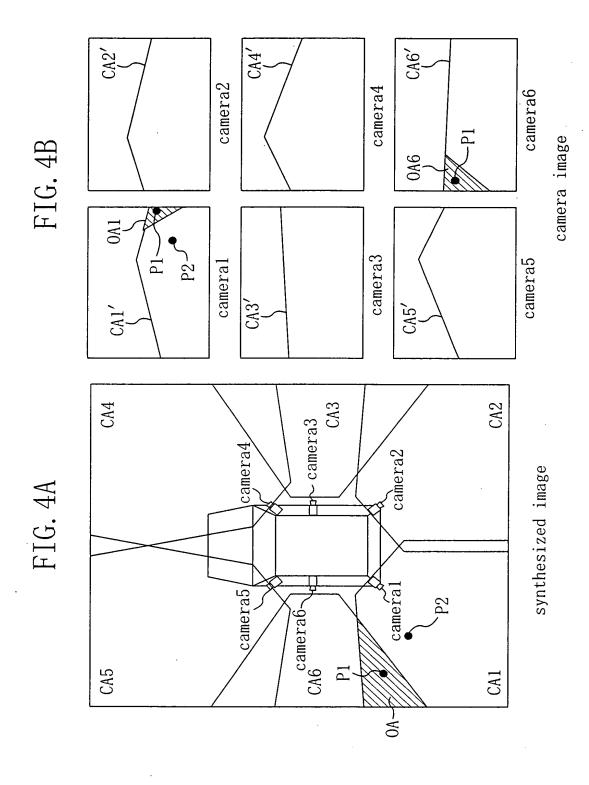
Inventor: HIROFUMI ISHII, ET AL Atty. Ref. No.: 5077-000057

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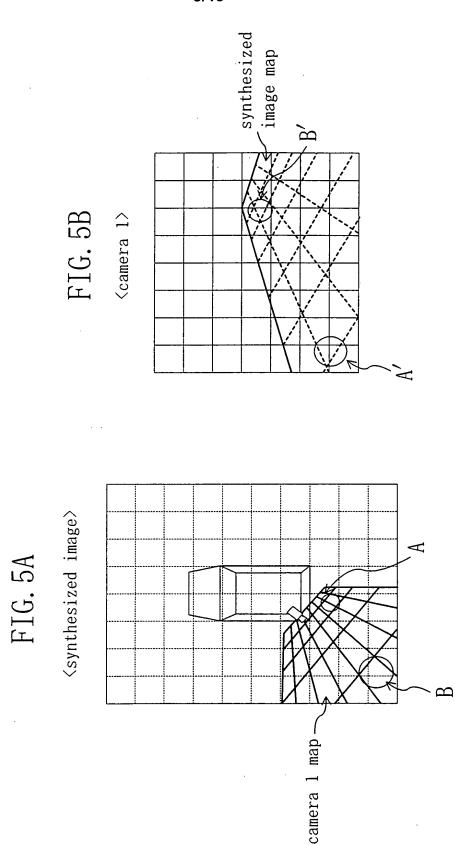
FIG. 3



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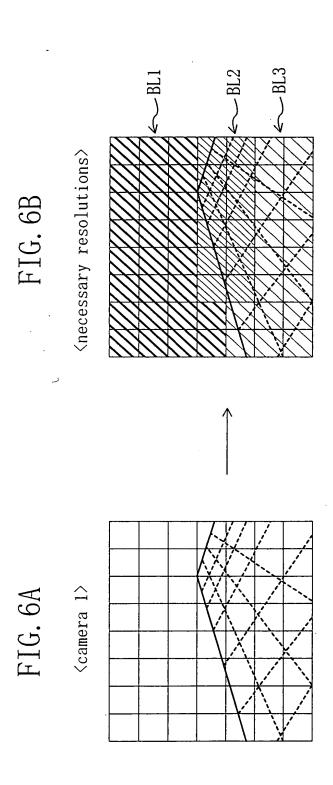
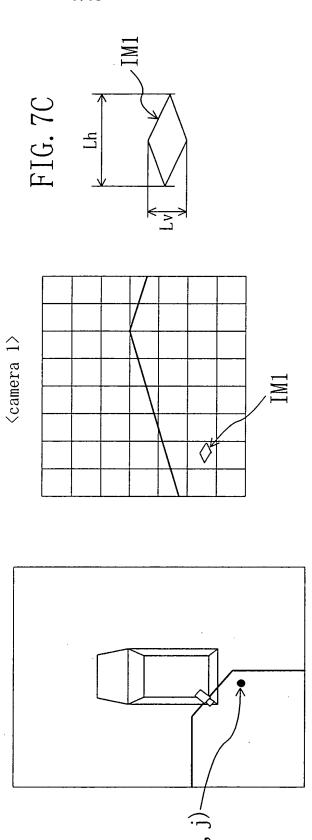


FIG. 7B

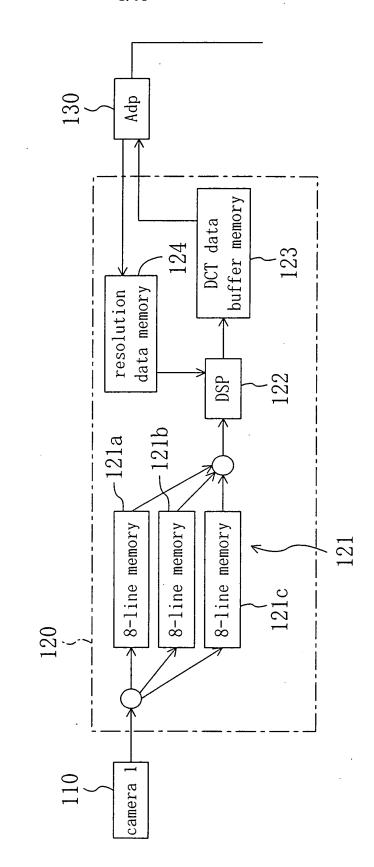
<synthesized image>

FIG. 7A

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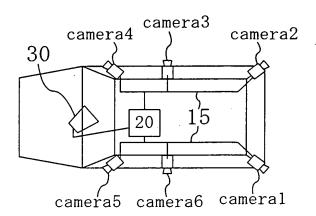


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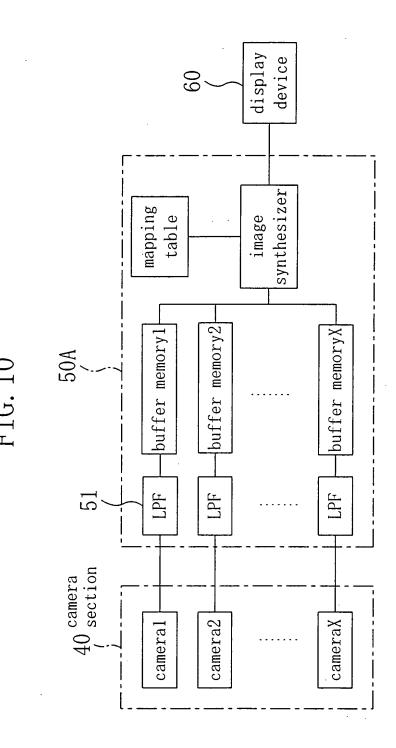


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FIG. 9



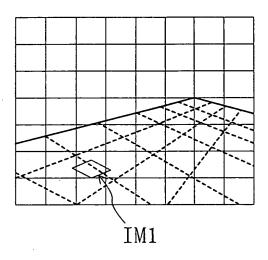
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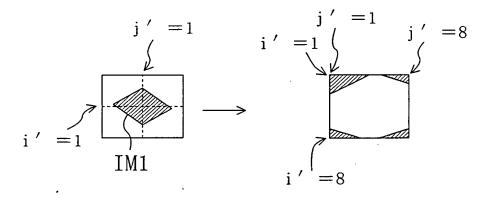
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## FIG. 11A

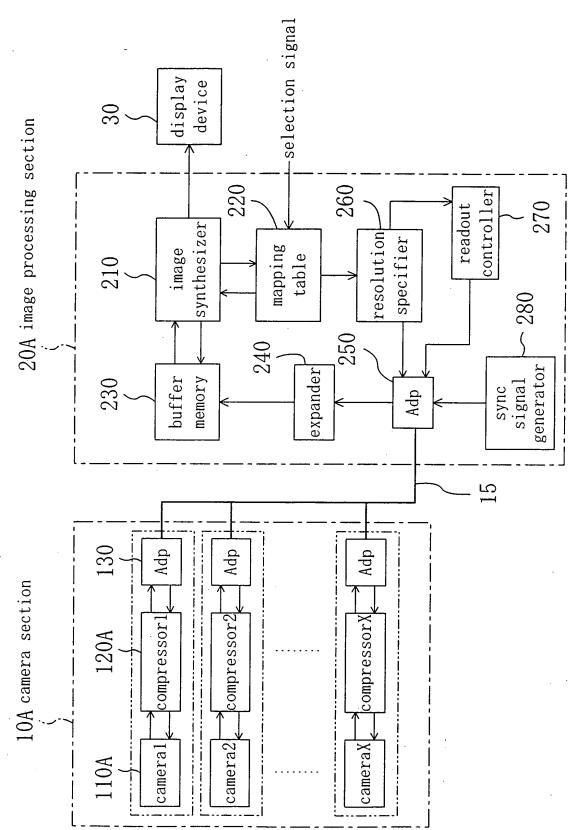
<camera 1>



## FIG. 11B



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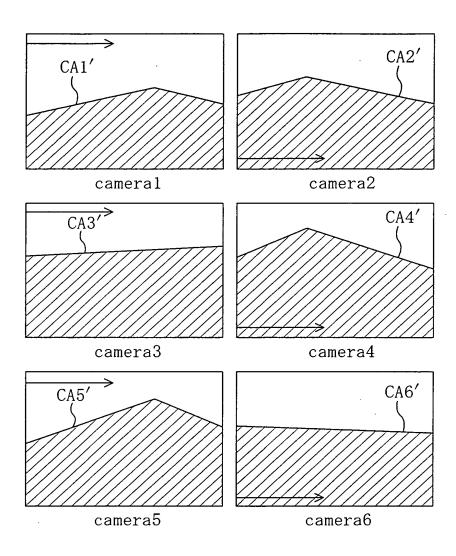
13/19 Adp buffer memory DCT data 124 123 125 area/resolution data readout control and sync 122 DSP signals 8-line memory 8-line memory 8-line memory 121 AD CCD synchronization controller readout/



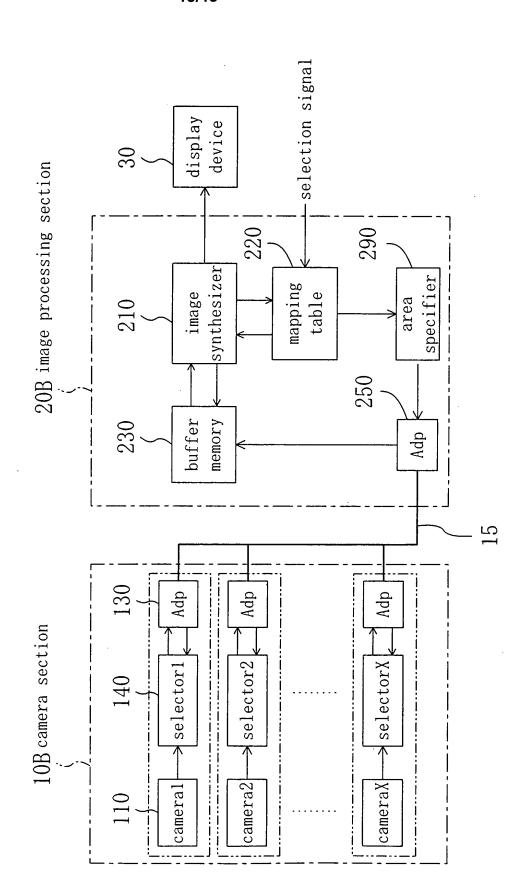


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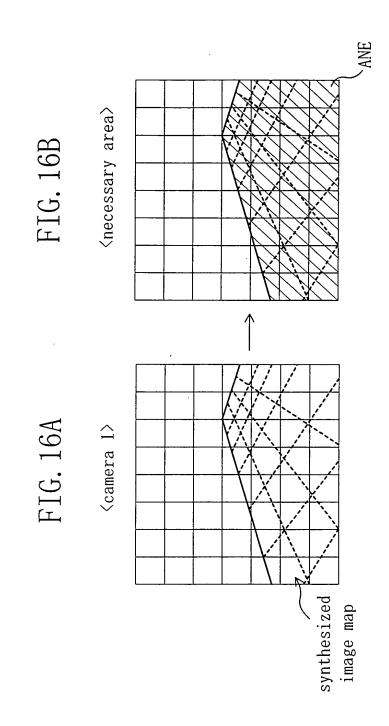
## FIG. 14



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FIG. 17B

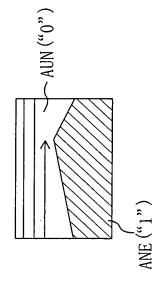
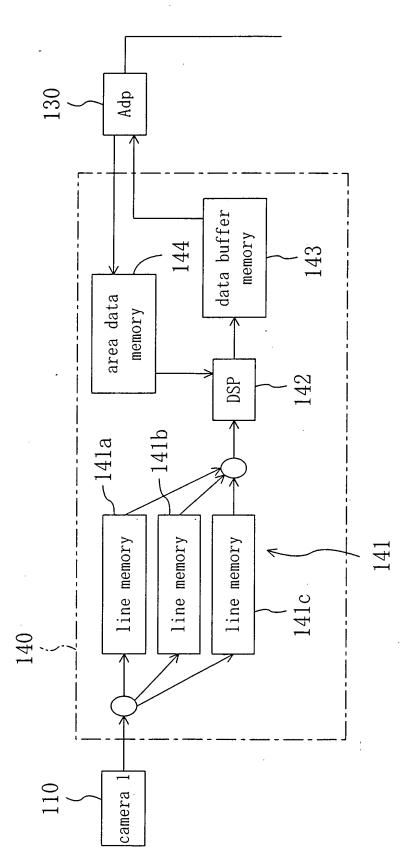


FIG. 17A

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